

XTREME 32

32 Port Fan-Out L-Band + S-Band RF Matrix Switch



XTREME 32 Dual Band Matrix Switch

General Description:

The **XTREME 32** matrix switch is a full fan-out (distributive) non-blocking signal management solution that routes an input to any or all outputs. The design features an industry exclusive architecture that supports both symmetric and asymmetric configurations of 32 combined inputs and outputs in a compact 1 RU chassis. Hot-Swappable redundant power supplies, I/O Modules, and a field replaceable cooling fan provide maximum reliability.

Features & Benefits:

- 950-3500 MHz Operating Range
- Hot-swappable Input and Output Adapters
- Flexible Matrix Configurations (16x16)
- Adjustable Input and Output Gain
- Redundant Hot Swappable Power Supplies
- Dual Gigabit Ethernet Ports
- Field Replaceable Cooling Fan

Specifications: ^{*1}	XTREME 32	
Operating Frequency:	950-2150 MHz	950-3500 MHz
Configurations:	16x16	
Input Gain Range:	-19.5 to 12 dB in .5 dB steps	
Output Gain Range:	-20.5 to 11 dB in .5 dB steps	
Impedance:	75 Ω or 50 Ω	
Input P1dB:	0 dBm	
OIP3:	10 dBm Min.	8 dBm Min.
Frequency Response:	+/- 2.0 dB +/- 0.5 dB Max. (Over any 36 MHz Channel)	
Isolation (input-to-input):	60 dB	
Isolation (output-to-output):	60 dB	
Isolation (input-to-output):	50 dB	45 dB
Input Return Loss:	14 dB	
Output Return Loss:	14 dB	
Noise Figure:		
Default Gain:	13 dB max	14 dB max
Max Input Gain:	9 dB Typical*	10 dB Typical*
RF Connectors:	F-Type, BNC 75 Ω or 50 Ω, SMA, or Mixed	
Power Requirements:	100-240 VAC Autoranging, 50/60 Hz	
Power Consumption:	100W Typical	
Local Control:	Front Panel 2.2" LCD Display with Rotary Switch Joystick	
Remote Control:	SNMP, TELNET, TCP/IP, Web Browser Interface Via Ethernet, Remote Panel	
Size:	1 RU: 1.75"H x 19"W x 18.5 D"	

Specifications may vary with connector type. See individual specification sheet for specific performance data.

* Typical refers to expected product performance that is useful in application of the product but is not covered by the product warranty